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10013089
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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10013089	12/07/2001	257	720+	2813	Dolan

**APPLICANTS: Pan Jeng-Yang; Chen Hung-Fa;

**CONTINUING DATA VERIFIED:

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**FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 U.S.C. 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no		67,200-627
Verifier and Acknowledged Examiner's initials			

TITLE : Semiconductor wafer tray positioning

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claims for O.G.
		DRAWING		
		Sheets Drawn:	Fig.s Drawn:	Print Fig.
		Application Examiner		
TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
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